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U.S. UTILITY Patent Application

PATENT NUMBER and  
ISSUE DATE

APPL NUM 10039707	FILING DATE 11/09/2001	CLASS 710 743	SUBCLASS 310	GAU 2134	EXAMINER Wong C. LEE
<b>**APPLICANTS:</b> Jaramillo Ken; Wu Shih; Ahern Frank;					
<b>**CONTINUING DATA VERIFIED:</b> NONE CEC					
<b>** FOREIGN APPLICATIONS VERIFIED:</b> NONE CEC					
PG-PUB <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/> RESCIND <input type="checkbox"/>					
Foreign priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no 35 USC 119 conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no Verified and Acknowledged Examiners's initials CEC				ATTORNEY DOCKET NO 101950.00076	
TITLE : Method for increasing peripheral component interconnect (PCI) bus throughput via a bridge for memory read transfers via dynamic variable prefetch					
U.S. DEPT. OF COMM./PAT. & TM-PTO-436L (Rev. 12-96)					

<b>NOTICE OF ALLOWANCE MAILED</b>		<b>CLAIMS ALLOWED</b>	
<b>ISSUE FEE</b>		<b>Assistant Examiner</b>	
Amount Due	Date Paid	<b>Primary Examiner</b>	
<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>		<b>PREPARED FOR ISSUE</b>	
		<b>Application Examiner</b>	
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